OLED DISPLAY SPECIFICATION

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REX012864M

General Specification

The features is described as follow:

- Dot Matrix: 128 × 64 Dots
- Module Dimension: 26.7 × 19.26 × 1.26 (mm)
- Active Area: 21.744 × 11.204 (mm)
- Pixel Size: 0.148 × 0.148 (mm)
- Pixel Pitch: 0.17 × 0.17 (mm)
- Display Mode: Passive Matrix
- Display Color: Monochrome
- Drive Duty: 1/64 Duty
- IC: SSD1306
- Interface: 6800,8080,SPI,I2C
- Size: 0.96-inch

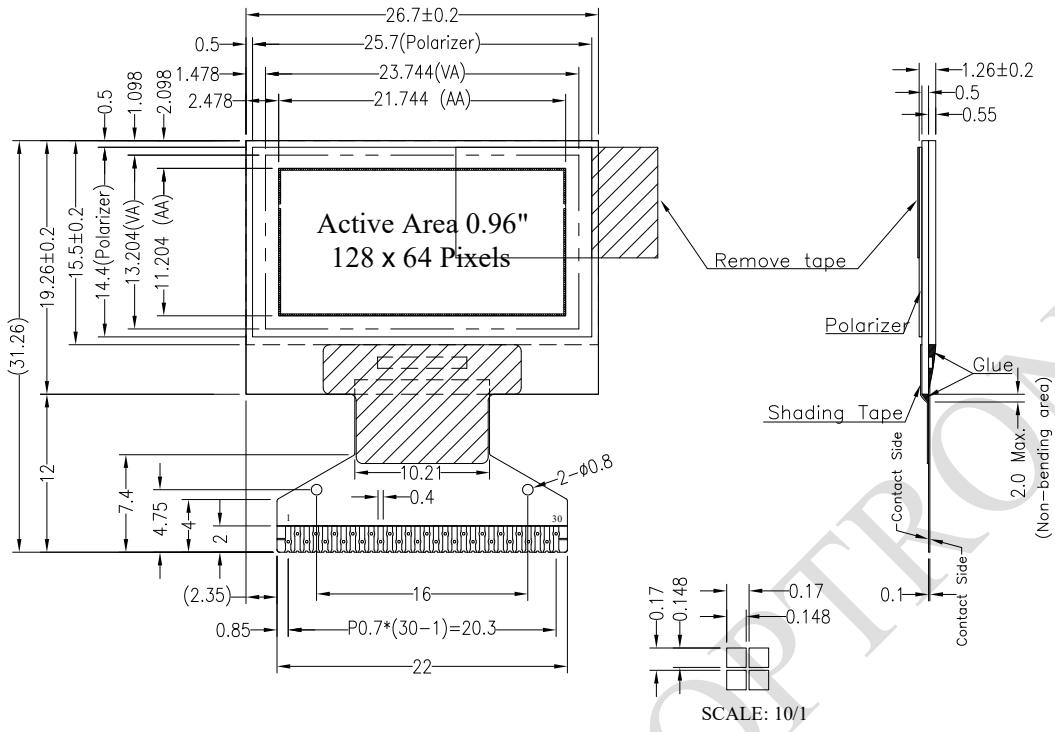
Interface Pin Function

<table>
<tr><th>Pin No.</th><th>Symbol</th><th>Function</th></tr>
<tr><td>1</td><td>N.C.
(GND)</td><td>Reserved Pin (Supporting Pin)
The supporting pins can reduce the influences from stresses on the function pins. These pins must be connected to external ground.</td></tr>
<tr><td>2</td><td>C2N</td><td rowspan="4">Positive Terminal of the Flying Inverting Capacitor Negative Terminal of the Flying Boost Capacitor The charge-pump capacitors are required between the terminals. They must be floated when the converter is not used.</td></tr>
<tr><td>3</td><td>C2P</td></tr>
<tr><td>4</td><td>C1P</td></tr>
<tr><td>5</td><td>C1N</td></tr>
<tr><td>6</td><td>VBAT</td><td>Power Supply for DC/DC Converter Circuit
This is the power supply pin for the internal buffer of the DC/DC voltage converter. It must be connected to external source when the converter is used. It should be connected to VDD when the converter is not used.</td></tr>
<tr><td>7</td><td>NC</td><td>NC</td></tr>
<tr><td>8</td><td>VSS</td><td>Ground of Logic Circuit
This is a ground pin. It acts as a reference for the logic pins. It must be connected to external ground.</td></tr>
<tr><td>9</td><td>VDD</td><td>Power Supply for Logic
This is a voltage supply pin. It must be connected to external source.</td></tr>
<tr><td>10</td><td>BS0</td><td rowspan="3">Communicating Protocol Select
These pins are MCU interface selection input. See the following table:
<table><tr><th></th><th>BS0</th><th>BS1</th><th>BS2</th></tr><tr><td>I2C</td><td>0</td><td>1</td><td>0</td></tr><tr><td>3-wire SPI</td><td>1</td><td>0</td><td>0</td></tr><tr><td>4-wire SPI</td><td>0</td><td>0</td><td>0</td></tr><tr><td>8-bit 68XX Parallel</td><td>0</td><td>0</td><td>1</td></tr><tr><td>8-bit 80XX Parallel</td><td>0</td><td>1</td><td>1</td></tr></table></td></tr>
<tr><td>11</td><td>BS1</td></tr>
<tr><td>12</td><td>BS2</td></tr>
<tr><td>13</td><td>CS#</td><td>Chip Select
This pin is the chip select input. The chip is enabled for MCU communication only when CS# is pulled low.</td></tr>
<tr><td>14</td><td>RES#</td><td>Power Reset for Controller and Driver
This pin is reset signal input. When the pin is low, initialization of the chip</td></tr>
</table>

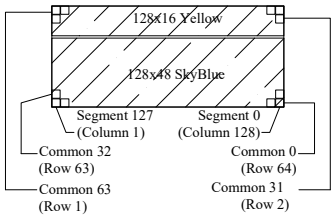
		is executed.
15	D/C#	*Data/Command Control* This pin is Data/Command control pin. When the pin is pulled high, the input at D7~D0 is treated as display data. When the pin is pulled low, the input at D7~D0 will be transferred to the command register. For detail relationship to MCU interface signals, please refer to the Timing Characteristics Diagrams. When the pin is pulled high and serial interface mode is selected, the data at SDIN is treated as data. When it is pulled low, the data at SDIN will be transferred to the command register. In I2C mode, this pin acts as SA0 for slave address selection.
16	R/W#	*Read/Write Select or Write* This pin is MCU interface input. When interfacing to a 68XX-series microprocessor, this pin will be used as Read/Write (R/W#) selection input. Pull this pin to “High” for read mode and pull it to “Low” for write mode. When 80XX interface mode is selected, this pin will be the Write (WR#) input. Data write operation is initiated when this pin is pulled low and the CS# is pulled low.
17	E/RD#	*Read/Write Enable or Read* This pin is MCU interface input. When interfacing to a 68XX-series microprocessor, this pin will be used as the Enable (E) signal. Read/write operation is initiated when this pin is pulled high and the CS# is pulled low. When connecting to an 80XX-microprocessor, this pin receives the Read (RD#) signal. Data read operation is initiated when this pin is pulled low and CS# is pulled low.
18~25	D0~D7	*Host Data Input/Output Bus* These pins are 8-bit bi-directional data bus to be connected to the microprocessor’s data bus. When serial mode is selected, D1 will be the serial data input SDIN and D0 will be the serial clock input SCLK. When I2C mode is selected, D2 & D1 should be tired together and serve as SDAout & SDAin in application and D0 is the serial clock input SCL.

26	IREF	*Current Reference for Brightness Adjustment* This pin is segment current reference pin. A resistor should be connected between this pin and VSS. Set the current lower than 30uA.
27	VCOMH	*Voltage Output High Level for COM Signal* This pin is the input pin for the voltage output high level for COM signals. A capacitor should be connected between this pin and VSS.
28	VCC	*Power Supply for OEL Panel* This is the most positive voltage supply pin of the chip. A stabilization capacitor should be connected between this pin and VSS when the converter is used. It must be connected to external source when the converter is not used.
29	VLSS	*Ground of Analog Circuit* This is an analog ground pin. It should be connected to VSS externally.
30	NC (GND)	*Reserved Pin (Supporting Pin)* The supporting pins can reduce the influences from stresses on the function pins. These pins must be connected to external ground.

Contour Drawing



PIN	SYMBOL	PIN	SYMBOL
1	NC(GND)	14	RES#
2	C2N	15	D/C#
3	C2P	16	R/W#
4	C1P	17	E/RD#
5	C1N	18	D0
6	VBAT	19	D1
7	NC	20	D2
8	VSS	21	D3
9	VDD	22	D4
10	BS0	23	D5
11	BS1	24	D6
12	BS2	25	D7
13	CS#	26	IREF
		27	VCOMH
		28	VCC
		29	VLSS
		30	NC(GND)



The non-specified tolerance of dimension is ±0.3mm.

Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit
Supply Voltage for Logic	VDD	0	4	V
Charge Pump Regulator Supply Voltage	VBAT	-0.3	5.0	V
Supply Voltage for Display	VCC	0	16.0	V
Operating Temperature	TOP	-40	+80	°C
Storage Temperature	TSTG	-40	+85	°C

Electrical Characteristics

DC Electrical Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage for Logic	VDD	—	2.8	3.0	3.3	V
Supply Voltage for Display (Supplied Externally)	VCC	—	11.5	12	12.5	V
Charge Pump Regulator Supply Voltage	VBAT	—	3.0	—	4.2	V
Charge Pump Output Voltage for Display (Generated by Internal DC/DC)	Charge Pump VCC	—	7.0	7.5	—	V
Input High Volt.	VIH	—	0.8×VDD	—	VDD	V
Input Low Volt.	VIL	—	0	—	0.2×VDD	V
Output High Volt.	VOH	—	0.9×VDD	—	VDD	V
Output Low Volt.	VOL	—	0	—	0.1×VDD	V
Operating Current for VCC (50% display ON) (VCC Supplied Externally)	ICC	VCC =12V	-	9	13.5	mA
50% check Board operating Current (VCC Generated by Internal DC/DC)	IBAT	—	—	15	25	mA