

OLED DISPLAY SPECIFICATION



RAYSTAR

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REX009632C-ZIF

General Specification

The features is described as follow:

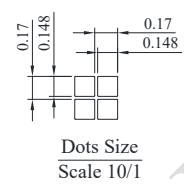
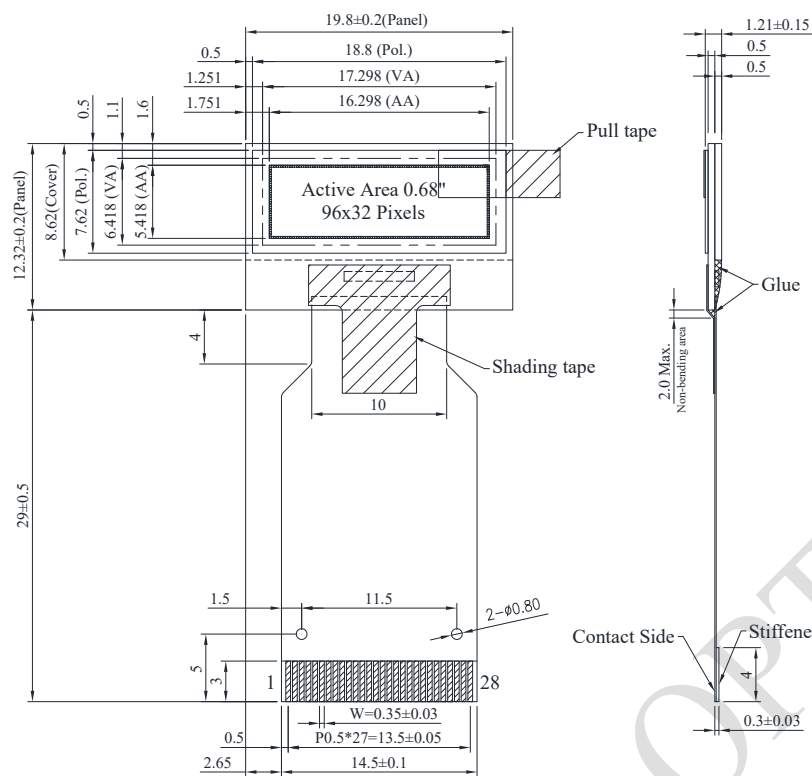
- Module Dimension: 19.80 x 12.32 x 1.21 mm
- Active Area: 16.298 x 5.418 mm
- Dot Matrix: 96 x 32
- Dot Size: 0.148 x 0.148 mm
- Dot Pitch: 0.17 x 0.17 mm
- Display Mode: Passive Matrix
- Duty: 1/32 Duty
- Display Color: Monochrome
- Controller IC: SSD1315
- Interface: 6800,8080,4-wire SPI,I2C
- Size: 0.68-inch

Interface Pin Function

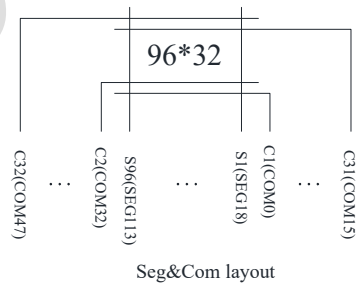
Pin No.	Symbol	Function										
1	VSS	Ground pin. It must be connected to external ground.										
2	NC	No connection										
3	VDD	Power supply pin for core logic operation.										
4~8	NC	No connection										
9	VDD	Power supply pin for core logic operation.										
10	BS1	MCU bus interface selection pins. Select appropriate logic setting as described in the following table. BS2, BS1 are pin select <table><tr><td>BS[2:1]</td><td>Interface</td></tr><tr><td>00</td><td>4 line SPI</td></tr><tr><td>01</td><td>I2C</td></tr><tr><td>11</td><td>8-bit 8080 parallel</td></tr><tr><td>10</td><td>8-bit 6800 parallel</td></tr></table>	BS[2:1]	Interface	00	4 line SPI	01	I2C	11	8-bit 8080 parallel	10	8-bit 6800 parallel
BS[2:1]	Interface											
00	4 line SPI											
01	I2C											
11	8-bit 8080 parallel											
10	8-bit 6800 parallel											
11	BS2	Note										
12	NC	No connection										
13	CS#	This pin is the chip select input connecting to the MCU. The chip is enabled for MCU communication only when CS# is pulled LOW (active LOW).										
14	RES#	This pin is reset signal input. When the pin is pulled LOW, initialization of the chip is executed. Keep this pin HIGH (i.e. connect to VDD) during normal operation.										
15	D/C#	This pin is Data/Command control pin connecting to the MCU. When the pin is pulled HIGH, the data at D[7:0] will be interpreted as data. When the pin is pulled LOW, the data at D[7:0] will be transferred to a command register. In I2C mode, this pin acts as SA0 for slave address selection.										
16	W/R#	This is read / write control input pin connecting to the MCU interface. When interfacing to a 6800-series microprocessor, this pin will be used as Read/Write (R/W#) selection input. Read mode will be carried out when this										

		pin is pulled HIGH (i.e. connect to VDD) and write mode when LOW. When 8080 interface mode is selected, this pin will be the Write (WR#) input. Data write operation is initiated when this pin is pulled LOW and the chip is selected. When serial or I2C interface is selected, this pin must be connected to VSS.
17	E/RD#	This pin is MCU interface input. When 6800 interface mode is selected, this pin will be used as the Enable (E) signal. Read/write operation is initiated when this pin is pulled HIGH and the chip is selected. When 8080 interface mode is selected, this pin receives the Read (RD#) signal. Read operation is initiated when this pin is pulled LOW and the chip is selected. When serial or I2C interface is selected, this pin must be connected to VSS.

Contour Drawing



PIN	SYMBOL	PIN	SYMBOL
1	VSS	15	D/C#
2	NC	16	WR#
3	VDD	17	E/RD#
4	NC	18	D0
5	NC	19	D1
6	NC	20	D2
7	NC	21	D3
8	NC	22	D4
9	VDD	23	D5
10	BS1	24	D6
11	BS2	25	D7
12	NC	26	IREF
13	CS#	27	VCOMH
14	RES#	28	VCC



The non-specified tolerance of dimension is ±0.3 mm .

Absolute Maximum Ratings

Parameter	Symbol	Min	Typ.	Max	Unit
Supply Voltage for Logic	VDD	-0.3	-	4	V
Supply Voltage for Display	VCC	0	-	18	V
Operating Temperature	TOP	-40	-	+80	°C
Storage Temperature	TSTG	-40	-	+85	°C

Electrical Characteristics

DC Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage for Logic	VDD	—	1.65	3.0	3.3	V
Supply Voltage for Display	VCC	—	7.5	12.0	12.5	V
High Level Input	VIH	—	0.8×VDD	—	VDD	V
Low Level Input	VIL	—	0	—	0.2×VDD	V
High Level Output	VOH	I _{out} = 100uA	0.9×VDD	—	VDD	V
Low Level Output	VOL	I _{out} = 100uA	0	—	0.1×VDD	V
Display 50% Pixel on	ICC	VCC=12V	—	5.0	7.5	mA