

OLED DISPLAY SPECIFICATION



RAYSTAR

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REN025664D-CTP

General Specification

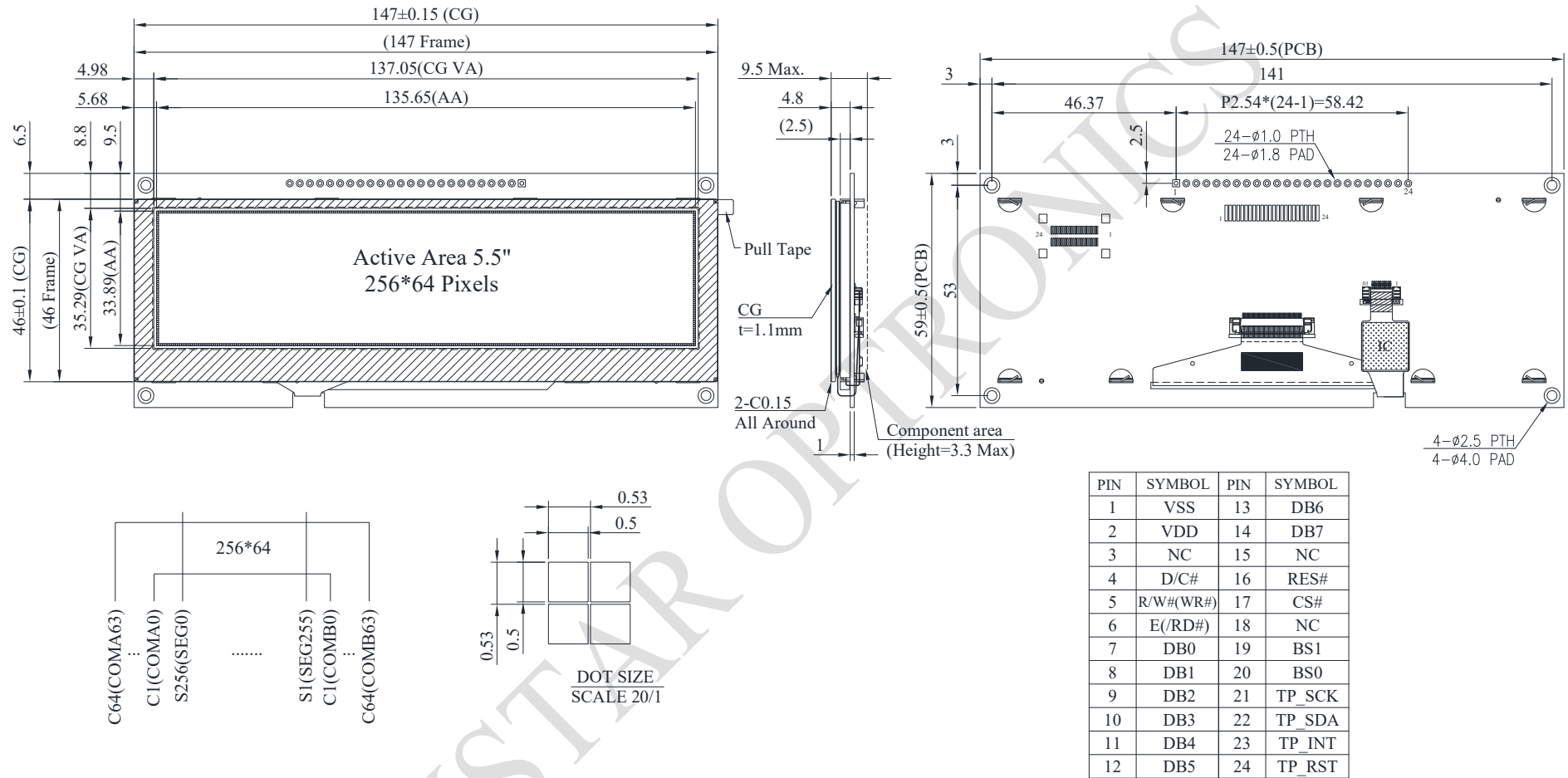
- Module dimension: 147.0 × 59.0 × 4.8 mm
- Active area: 135.65 x 33.89 mm
- Dot Matrix : 256 × 64
- Dot Size: 0.5 × 0.5 mm
- Dot Pitch: 0.53 × 0.53mm
- Display Mode: Passive Matrix
- Duty: 1/64
- Gray Scale: 4 bits
- Display Color: Monochrome
- OLED IC: SSD1322
- OLED Interface: 6800, 8080, SPI
- Size: 5.5 inch
- CTP IC: GT911
- Detect Point: 1
- CTP Interface: I2C
- Surface: Normal Glare

Interface Pin Function

Pin No.	Symbol	I/O	Function
1	VSS	P	Ground.
2	VDD	P	Power Supply for Core Logic Circuit Power supply pin for core logic operation. A capacitor is required to connect between this pin and VSS
3	N.C.	P	Reserved Pin The N.C. pin between function pins are reserved for compatible and flexible design.
4	D/C#	I	Data/Command Control This pin is Data/Command control pin connecting to the MCU. When the pin is pulled HIGH, the content at D[7:0] will be interpreted as data. When the pin is pulled LOW, the content at D[7:0] will be interpreted as command.
5	R/W# (WR#)	I	Read/Write Select or Write This pin is MCU interface input. When interfacing to a 68XX-series microprocessor, this pin will be used as Read/Write (R/W#) selection input. Pull this pin to "High" for read mode and pull it to "Low" for write mode. When 80XX interface mode is selected, this pin will be the Write (WR#) input. Data write operation is initiated when this pin is pulled low and the CS# is pulled low. When serial mode is selected, this pin must be connected to VSS.
6	E/RD#	I	Read/Write Enable or Read This pin is MCU interface input. When interfacing to a 68XX-series microprocessor, this pin will be used as the Enable (E) signal. Read/write operation is initiated when this pin is pulled high and the CS# is pulled low. When connecting to an 80XX-microprocessor, this pin receives the Read (RD#) signal. Data read operation is initiated when this pin is pulled low and CS# is pulled low. When serial mode is selected, this pin must be connected to VSS.
7~14	DB0 DB1 DB2 DB3 DB4 DB5 DB6 DB7	I/O	Host Data Input/Output Bus These pins are 8-bit bi-directional data bus to be connected to the microprocessor's data bus. When serial mode is selected, D1 will be the serial data input SDIN and D0 will be the serial clock input SCLK.
15	NC	P	Reserved Pin The N.C. pin between function pins are reserved for compatible and flexible design.
16	RES#	I	This pin is reset signal input.

			When the pin is pulled LOW, initialization of the chip is executed. Keep this pin pull HIGH during normal operation.										
17	CS#	I	Data/Command Control This pin is the chip select input connecting to the MCU. The chip is enabled for MCU communication only when CS# is pulled LOW.										
18	NC	P	Reserved Pin The N.C. pin between function pins are reserved for compatible and flexible design.										
19	BS1	I	Communicating Protocol Select These pins are MCU interface selection input. See the following table: <table><tr><th>BS[1:0]</th><th>Bus Interface Selection</th></tr><tr><td>00</td><td>4 line SPI</td></tr><tr><td>01</td><td>3 line SPI</td></tr><tr><td>10</td><td>8-bit 8080 parallel</td></tr><tr><td>11</td><td>8-bit 6800 parallel</td></tr></table>	BS[1:0]	Bus Interface Selection	00	4 line SPI	01	3 line SPI	10	8-bit 8080 parallel	11	8-bit 6800 parallel
BS[1:0]	Bus Interface Selection												
00	4 line SPI												
01	3 line SPI												
10	8-bit 8080 parallel												
11	8-bit 6800 parallel												
20	BS0												
21	TP_SCK	I	I2C clock input										
22	TP_SDA	I	I2C data input and output										
23	TP_INT	I	External interrupt to the host										
24	TP_RST	I	External Reset, Low is active										

Contour Drawing



The non-specified tolerance of dimension is ±0.3 mm .

Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit
Supply Voltage for Display	VDD	-0.3	4	V
Operating Temperature	TOP	-20	70	°C
Storage Temperature	TSTG	-30	80	°C

Electrical Characteristics

DC Electrical Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage for Logic	VDD	—	2.8	3.0	3.3	V
High Level Input	VIH	—	0.8×VDD	—	VDD	V
Low Level Input	VIL	—	0	—	0.2×VDD	V
High Level Output	VOH	—	0.9×VDD	—	VDD	V
Low Level Output	VOL	—	0	—	0.1×VDD	V
50% Clock Board operating Current	IDD	VDD =3V	—	240	400	mA