

OLED DISPLAY SPECIFICATION



RAYSTAR

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REA160128B

General Specification

- Module dimension: 34.90 × 57.80 × 5.36 Max.mm
- Active area: 28.908 × 39.34 mm
- Dot Matrix: 160 x 128
- Pixel size: 0.206 × 0.226 mm
- Pixel pitch: 0.226 × 0.246 mm
- Display Mode: Passive Matrix
- Duty: 1/128 Duty
- Gray Scale: 4 bit
- Display Color: Monochrome
- IC: CH1120
- Interface: 4-Wire SPI
- Size: 1.92 inch

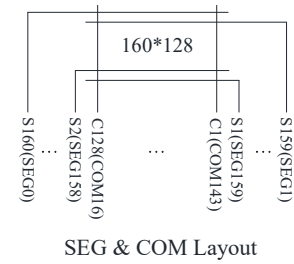
Interface Pin Function

No.	Symbol	Function
1	GND	Ground for logic and analog. This pad should be connected to GND externally.
2	VDD	Power supply for logic and input/output
3	SCL	serial clock input
4	SI	serial data input
5	$\overline{\text{RES}}$	This is a reset signal input pad. When RES is set to "L", the settings are initialized. The reset operation is performed by the RES signal level. This pin internal pull high.
6	A0	This is the Data/Command control pad that determines whether the data bits are data or a command. A0 = "H": the inputs at D0 and D1 are treated as display data. A0 = "L": the inputs at D0 and D1 are transferred to the command registers.
7	$\overline{\text{CS}}$	This pad is the chip select input. When CS = "L", then the chip select becomes active, and data/command I/O is enabled.

Technical drawing of a device with the following dimensions and labels:

- Top dimensions: 34.5 ± 0.2 , $33.5(\text{Pol.})$, $30.91(\text{VA})$, $28.908(\text{AA})$
- Right side dimensions: 5.36 Max. , 1.56 ± 0.2
- Labels: Pull Tape, Shading Tape
- Active Area: $1.92''$ 160×128
- Bottom dimensions: 34.9 ± 0.5 , 9.83 , $P2.54 * 6 = 15.24$, 30.9 , 57.8 ± 0.5 , 53.8 , 1 , (10) , (14.9)
- Bottom labels: Component area (Height=2.5 MAX), $4-\phi 2 \text{ PTH}$, $4-\phi 3.5 \text{ PAD}$
- Other labels: 7, 1, 24, 1

Dots Size
Scale 20/1



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Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit
Supply Voltage for Logic	VDD	-0.3	6.5	V
Operating Temperature	TOP	-40	+80	°C
Storage Temperature	TSTG	-40	+85	°C

Electrical Characteristics

DC Electrical Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage for Logic	VDD	—	3.0	3.3	5.2	V
Input High Volt.	VIH	—	0.8xVDD	—	VDD	V
Input Low Volt.	VIL	—	VSS	—	0.2xVDD	V
Output High Volt.	VOH	IOH=-0.5mA	0.8xVDD	—	VDD	V
Output Low Volt.	VOL	IOL=0.5mA	VSS	—	0.2xVDD	V
Display 50% Pixel on	IDD	VDD=3.3V	—	160	240	mA