

OLED DISPLAY SPECIFICATION



RAYSTAR

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REA012864AE

General Specification

- Module dimension: 42.4 x 38.0 x 5.36 (MAX) mm
- Active area: 35.05 × 17.51 mm
- Dot Matrix: 128 × 64
- Pixel Size: 0.249 × 0.249 mm
- Pixel Pitch: 0.274 × 0.274 mm
- Duty: 1/64 Duty
- Display Mode: Passive Matrix
- Display Color: Monochrome
- Interface: 4-Wire SPI
- IC: CH1116
- SIZE: 1.54 inch

Interface Pin Function

No.	Symbol	Function
1	GND	Ground.
2	VCC	Power supply input
3	D0	The serial clock input
4	D1	The serial data input
5	RES#	This is a reset signal input pad. When RES# is set to "L", the settings are initialized. The reset operation is performed by the RES# signal level.
6	D/C#	This is the Data/Command control pad that determines whether the data bits are data or a command. D/C# = "H": the input at D1 is treated as display data. D/C# = "L": the input at D1 is transferred to the command registers.
7	CS#	This pad is the chip select input. When CS# = "L", then the chip select becomes active, and data/command I/O is enabled.

Figure 1: Mechanical drawing of the active area and component area of the 128*64 dot matrix module. The drawing includes three views: a top view of the active area, a side view of the component area, and a bottom view of the component area. The top view shows the active area with dimensions 42.04±0.2 (Pol.), 37.05 (VA), 35.05 (AA), and 1.54 (Active Area). It also shows the pull tape, shading tape, and 4-ø3 PTH / 4-ø4.5 PAD. The side view shows the component area with dimensions 5.36 MAX, 1.56±0.2, 2.5, 38±0.5 (PCB), 33, 2.5, 1, and 1.54. The bottom view shows the component area with dimensions 42.4±0.5 (PCB), 13.58, P2.54*6=15.24, 7, 1, 24, 1, 14.2, 14, 37.4, 2.5, 17.78±0.5, P2.54*6=15.24, 5.1, 6, 2, 2.54, and 1. The drawing also includes a Seg&Com Layout table and a Dots Size Scale 10/1.

Seg&Com Layout	
128*64	C63 (COM62)
	C1 (COM0)
	S1 (SEG0)
	S128 (SEG127)
	C2 (COM1)
	C64 (COM63)

Dots Size Scale 10/1

The non specified tolerance of dimension is ±0.2mm

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Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit
Supply Voltage for Logic	VCC	0	7	V
Operating Temperature	TOP	-40	+80	°C
Storage Temperature	TSTG	-40	+85	°C

Electrical Characteristics

DC Electrical Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage for Logic	VCC	—	2.8	3.0	5.5	V
High Level Input	VIH	—	$0.8 \times VCC$	—	VCC	V
Low Level Input	VIL	—	VSS	—	$0.2 \times VCC$	V
High Level Output	VOH	—	$0.8 \times VCC$	—	VCC	V
Low Level Output	VOL	—	VSS	—	$0.2 \times VCC$	V
Display 50% Pixel on	ICC	VCC = 3.0V	—	60	120	mA