

OLED DISPLAY SPECIFICATION



RAYSTAR

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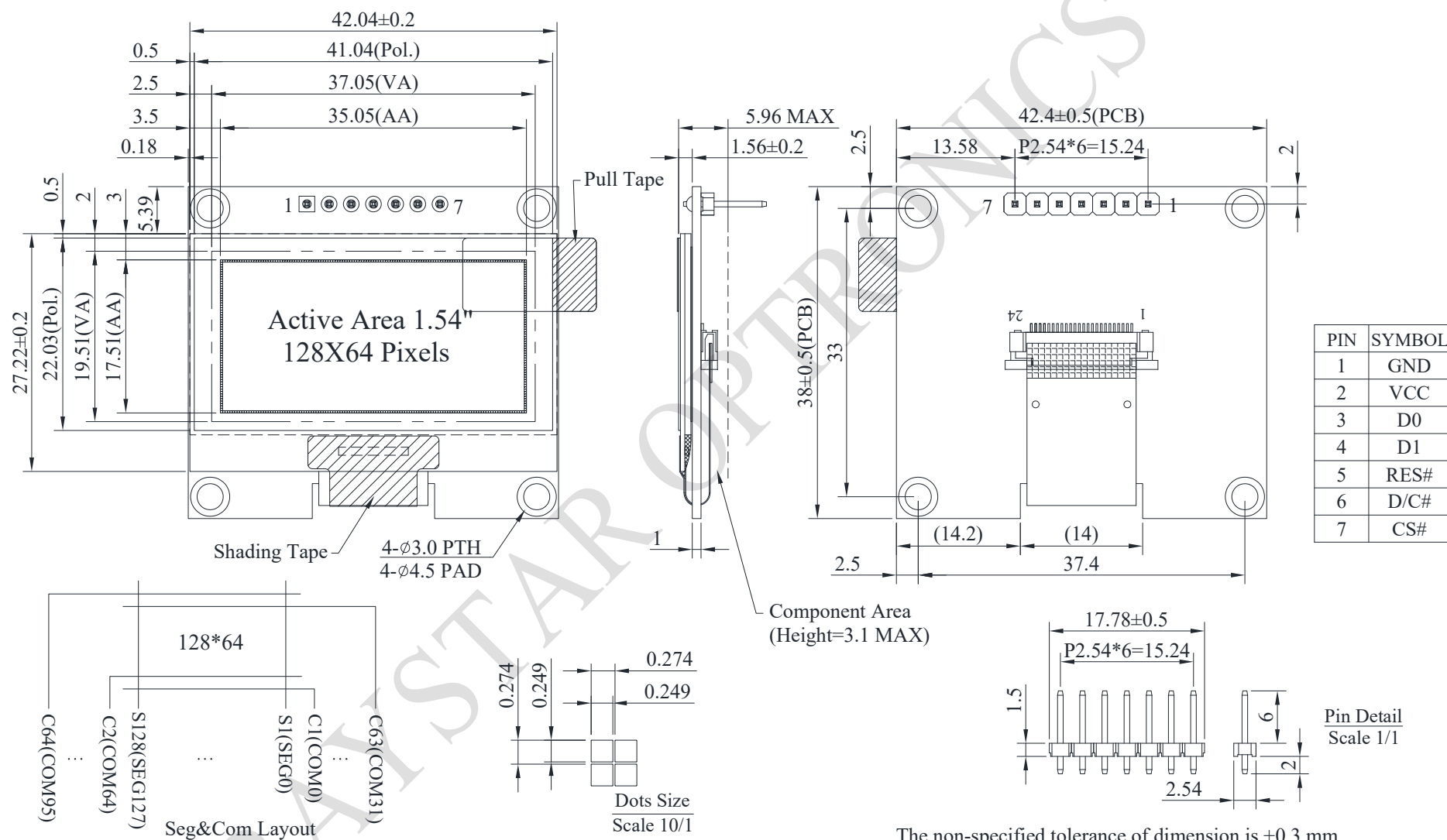
General Specification

- Module dimension: 42.40 x 38.00 x 5.96(MAX) mm
- Active area: 35.05 × 17.51 mm
- Dot Matrix: 128 × 64
- Dot size: 0.249 × 0.249 mm
- Dot pitch: 0.274 × 0.274 mm
- Display Mode : Passive Matrix
- Duty: 1/64 Duty
- Gray Scale: 4 bits
- Display Color: Monochrome
- IC: SSD1327
- Interface: 4-wire SPI
- Size: 1.54 inch

Interface Pin Function

No.	Symbol	Function
1	GND	This is a ground pin.
2	VCC	Power supply for panel driving voltage. This is also the most positive power voltage supply pin. It is supplied by external high voltage source.
3	D0	These pins are bi-directional data bus connecting to the MCU data bus. Unused pins are recommended to tie LOW. When serial interface mode is selected, D0 will be the serial clock input: SCLK; D1 will be the serial data input: SDIN and D2 should be kept NC.
4	D1	
5	RES#	This pin is reset signal input. When the pin is pulled LOW, initialization of the chip is executed. Keep this pin pull HIGH during normal operation.
6	D/C#	This pin is Data/Command control pin connecting to the MCU. When the pin is pulled HIGH, the data at D[7:0] will be interpreted as data. When the pin is pulled LOW, the data at D[7:0] will be transferred to a command register.
7	CS#	This pin is the chip select input connecting to the MCU. The chip is enabled for MCU communication only when CS# is pulled LOW (active LOW).

Contour Drawing



The non-specified tolerance of dimension is ± 0.3 mm .

Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit
Supply Voltage for Logic	VCC	2.0	5.5	V
Operating Temperature	TOP	-40	+80	°C
Storage Temperature	TSTG	-40	+85	°C

Electrical Characteristics

DC Electrical Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage for Logic(3V/5V)	VCC	—	2.8	3.3	5.2	V
Input High Volt.	VIH	—	0.8×VCC	—	VCC	V
Input Low Volt.	VIL	—	0	—	0.2×VCC	V
Output High Volt.	VOH	—	0.9×VCC	—	VCC	V
Output Low Volt.	VOL	—	0	—	0.1×VCC	V
Display 50% Pixel on	ICC	VCC=3.3V	—	60	120	mA