OLED DISPLAY SPECIFICATION

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REA012832P

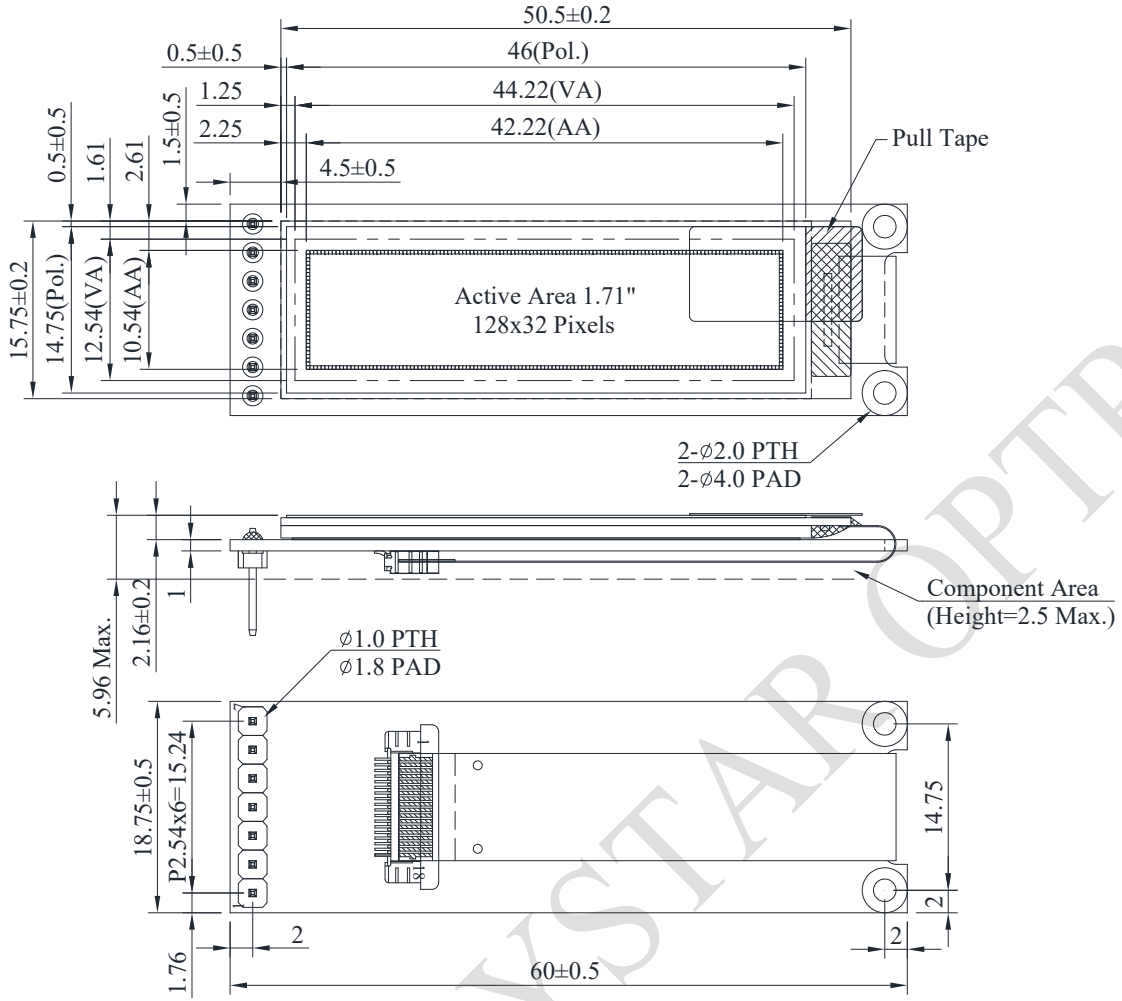
General Specification

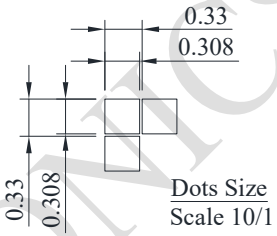
- Module dimension: 60.0 x 18.75 x 5.96 (MAX)
- Active area: 42.22 x 10.54 mm
- Dot Matrix: 128 x 32
- Pixel size: 0.308 x 0.308 mm
- Pixel pitch: 0.33 x 0.33 mm
- Display Mode: Passive Matrix
- Duty: 1/32 Duty
- Display Color: Monochrome
- IC: CH1115
- Interface: 4-Wire SPI
- Size: 1.71 inch

Interface Pin Function

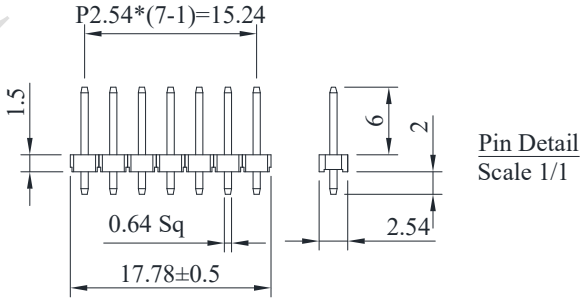
No.	Symbol	Function
1	GND	Ground.
2	VCC	Power supply pin for logic.
3	D0	When the serial interface is selected, then D0 serves as the serial clock input pad (SCL) and D1 serves as the serial data input pad (SI).
4	D1	
5	RES#	This is a reset signal input pad. When RES# is set to “L”, the settings are initialized. The reset operation is performed by the RES# signal level.
6	D/C#	This is the Data/Command control pad that determines whether the data bits are data or a command. D/C = “H”: the inputs at D0 and D1 are treated as display data. D/C = “L”: the inputs at D0 and D1 are transferred to the command registers.
7	CS#	This pad is the chip select input. When CS# = “L”, then the chip select becomes active, and data/command I/O is enabled.

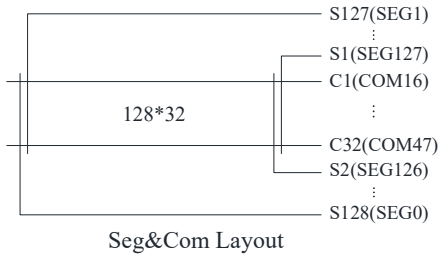
Contour Drawing





PIN	SYMBOL
1	GND
2	VCC
3	D0
4	D1
5	RES#
6	D/C#
7	CS#





The non-specified tolerance of dimension is ±0.3 mm .

Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit
Supply Voltage for Logic	VCC	-0.3	3.6	V
Operating Temperature	TOP	-40	+80	°C
Storage Temperature	TSTG	-40	+85	°C

Electrical Characteristics

DC Electrical Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage for Logic	VCC	—	1.8	3.0	3.3	V
High Level Input	VIH	—	0.8×VCC	—	VCC	V
Low Level Input	VIL	—	GND	—	0.2×VCC	V
High Level Output	VOH	—	0.8×VCC	—	VCC	V
Low Level Output	VOL	—	GND	—	0.2×VCC	V
Display 50% Pixel on	ICC	VCC =3V	—	35	70	mA